



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of:

INVENTORS: Hooli et al.

SERIAL NO.: 10/783,049

ART UNIT:

FILED: 2/19/04

EXAMINER:

TITLE: Chip-Level or Symbol-Level Equalizer Structure for Multiple Transmit  
And Receiver Antenna Configuration"

ATTORNEY DOCKET NO.: 872.0178.U1(US)

Commissioner For Trademarks

P.O. Box 1450

Alexandria, VA 22313-1450

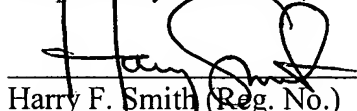
INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to Sections 609 and 707.05(b) of the MPEP and 37 CFR 1.97-1.99, copies are herewith submitted of documents which may be pertinent to the invention as claimed in the above-identified application. The attached form PTO-1449 lists these documents.

The citation of these documents should not be construed as a representation that a thorough search has been made, or that other, more pertinent material is not available.

Respectfully submitted,



Harry F. Smith (Reg. No.)

HARRINGTON & SMITH, LLP

4 Research Drive

Shelton, CT 06484-6212

Telephone: (203)925-9400

Facsimile: (203)944-0245

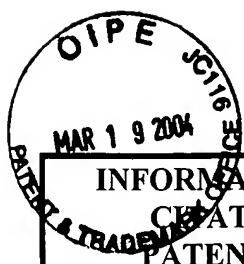
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CERTIFICATE OF MAILING

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**INFORMATION DISCLOSURE  
CITATION FORM FOR  
PATENT APPLICATION  
(FORM PTO-1449)  
(Substitute)**

Docket No.: 872.0178.U1(US)

Serial No.: 10/783,049

Applicant(s): Hooli, et al.

Filing Date: 2/19/04

Group:

**U.S. PATENT DOCUMENTS**

| Examiner<br>Initials | Document Number<br>(Number-Kind Code) | Publication Date<br>(MM-DD-YYYY) | Name of Patentee or Applicant | Class | Sub-class |
|----------------------|---------------------------------------|----------------------------------|-------------------------------|-------|-----------|
|                      | US-                                   |                                  |                               |       |           |
|                      | US-                                   |                                  |                               |       |           |
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|                      | US-                                   |                                  |                               |       |           |

**FOREIGN PATENT DOCUMENTS**

| Examiner<br>Initials | Document Number<br>(Country Code-Number-Kind Code) | Publication Date<br>(MM-DD-YYYY) | Name Of Patentee of Applicant | Translation?<br>Yes/No/n/a |
|----------------------|--|----------------------------------|-------------------------------|----------------------------|
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|                      | - -  |                                  |                               |                            |
|                      | - -  |                                  |                               |                            |

**OTHER DOCUMENTS (Author (Capitalize), Title, Date, Pages, Etc., if known)**

|  |   |
|--|---|
|  | "Chip-Level Channel Equalization in WCDMA Downlink", Kari Hooli et al., EURASIP Journal on Applied Signal Processing 2002-8, pp. 757-770                  |
|  | "A Generalized RAKE Receiver for Interference Suppression", Gregory E. Bottomley, et al., IEEE 2000, pp. 1536-1545  |
|  | "V-BLAST: An Architecture for Realizing Very High Data Rates Over the Rich-Scattering Wireless Channel", P.W. Wolniansky, et al., IEEE 1998, pp. 295-300. |
|  | "Pilot-aided Adaptive Chip Equalizer Receiver for Interference Suppression in DS-CDMA Forward Link", Frederik Petre et al., IEEE 2000, pp. 303-308        |
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Examiner's Signature:

Date Considered:

Initial if reference was considered, whether or not citation is in conformance with MPEP. Mark through citation if not considered.  
Include a copy of this citation form with your next correspondence to the Applicant(s).